

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit of parent application: 2841

Examiner of parent application: Ishwar B Patel

In Re PATENT APPLICATION Of:

Applicants : Norio TAKAHASHI

Appln. No. : To Be Assigned

Div of Ser. No : 10/289,358, which is division of  
10/022,285, which is division of  
09/450,504

Filed : Herewith

For : MODULE CIRCUIT BOARD FOR  
SEMICONDUCTOR DEVICE  
HAVING BARRIERS TO ISOLATE  
I/O TERMINALS FROM SOLDER

Attorney Ref.: F97ED0740-DIV3

**PRELIMINARY**  
**AMENDMENT**

**BOX: Non-Fee Amendment**

## Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

February 11, 2004

Sir:

Preliminary to examination, please amend the application as follows: